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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: ~~10/783,031~~ ^{10/780,031} Confirmation No.: Unknown
First Named Inventor: Park, Jongmin Filing Date: 17 February 2004
Group Art Unit: Unknown Examiner: Unknown
Atty. Docket No.: R-0003 US
Title: Technique for Programming Floating-gate Transistor Used in
Circuitry Such As Flash EPROM
Assignee(s): Mosel Vitelic Corp.

Mountain View, California
26 July 2004

COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97(b)

Sir:

Pursuant to 37 CFR 1.56, 1.97, and 1.98, each document listed on the accompanying substitute PTO Form 1449 is called to the attention of the Examiner for the above patent application. A copy of each listed document is enclosed.

Citation of each listed document shall not be construed as:

1. an admission that the document is necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described herein;
or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b).

U.S. patent application 10/640,929 is cited in paragraph 50 of the specification of the present application. The co-filed U.S. patent application cited in paragraph 57 of the specification of the present application is U.S. patent application 10/780,030.

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This information disclosure statement is submitted under the provisions of
37 CFR 1.97(b).

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Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Application No.	
				R-0003 US		10/780,031	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				First Named Inventor:		Confirmation No.	
Substitute PTO Form 1449				Park, Jongmin		Unknown	
Filing Date				Group			
17 February 2004				Unknown			

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	4,888,734	12/1989	Lee et al.	365	185	
	AB	6,040,216 B1	03/2000	Sung	438	257	
	AC	6,194,269 B1	02/2001	Sung et al.	438	258	
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	AF	10/640,929		Kim			08/2003
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	AH						
	AI						
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	AO						
	AP						

Foreign Patent Documents							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AQ							
	AR							
	AS							
	AT							
	AU							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
AV	Nation et al., "Architectural Impacts on Flash Endurance and Flash Data Retention", AMD Flash Technology White Paper, Rev. 37, Advanced Micro Drives, 7 November 1999, pp. 1 - 17
AW	
AX	

Examiner	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.